

	L #	Hits	Search Text	DBs
1	L1	4	("4909743" "5090912" "6053752" "5090911").pn.	USP AT; US-P GPU B
2	L2	3	("3673545" "4808115" "5807145").PN.	USP AT
3	L3	0	6053752.URPN.	USP AT
4	L4	8	5090912.URPN.	USP AT
5	L5	4	("3264597" "4201432" "4533203" "4776804").PN.	USP AT
6	L7	6	3264597.URPN.	USP AT
7	L8	10	("3524108" "4150863" "4264114" "4392705" "4505035" "4628410" "4631637" "4637135" "4655518" "4660911").PN.	USP AT
8	L9	16	4909743.URPN.	USP AT
9	L10	795	439/79.ccls.	USP AT; US-P GPU B
10	L11	728	10 and ((printed adj circuit) board\$1 substrate\$1 pcb\$1 motherboard\$1 back\$1plane\$1)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
11	L12	594	11 and spac\$3	USP AT; US-P GPU B
12	L13	134	11 not 12	USP AT; US-P GPU B
13	L14	7	("4659155" "4871321" "4946392" "5037314" "5044994" "5090912" "5169343").PN.	USP AT
14	L15	4	("3334325" "3399372" "3660803" "4223968").PN.	USP AT
15	L16	45	4659155.URPN.	USP AT
16	L17	4	("3334325" "3399372" "3660803" "4223968").PN.	USP AT
17	L18	7	5397241.URPN.	USP AT
18	L19	128	439/80.ccls.	USP AT; US-P GPU B
19	L20	101	19 not 11	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
20	L21	356	439/82.ccls.	USP AT; US-P GPU B
21	L22	338	21 not (11 20)	USP AT; US-P GPU B
22	L23	119	361/791.ccls.	USP AT; US-P GPU B
23	L24	6	("2701346" "2925537" "4076356" "4418972" "4482937" "4514784").PN.	USP AT
24	L25	57	4686607.URPN.	USP AT
25	L26	214	361/788.ccls.	USP AT; US-P GPU B
26	L27	201	26 not (11 20 23)	USP AT; US-P GPU B
27	L28	104	361/804.ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
28	L29	98	28 not (11 20 23 27)	USP AT; US-P GPU B
29	L30	501	361/785.ccls.	USP AT; US-P GPU B
30	L31	425	30 not (11 20 23 27 29)	USP AT; US-P GPU B
31	L32	345	31 and (connector\$1 socket\$1)	USP AT; US-P GPU B
32	L33	502	361/803.ccls.	USP AT; US-P GPU B
33	L34	380	33 not (11 20 23 27 29 31)	USP AT; US-P GPU B
34	L35	265	361/790.ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
35	L36	146	35 not (11 20 23 27 29 31 34)	USP AT; US-P GPU B
36	L37	257	(361/742,744).ccls.	USP AT; US-P GPU B
37	L38	191	37 not (11 20 23 27 29 31 34 36)	USP AT; US-P GPU B
38	L39	880	(361/792,795).ccls.	USP AT; US-P GPU B
39	L40	771	39 not (11 20 23 27 29 31 34 36 38)	USP AT; US-P GPU B
40	L41	162	40 and (flexible compliant)	USP AT; US-P GPU B
41	L42	302	439/91.ccls.	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
42	L43	273	42 not (11 20 23 27 29 31 34 36 38 41)	USP AT; US-P GPU B
43	L44	365	174/267.ccls.	USP AT; US-P GPU B
44	L45	335	44 not (11 20 23 27 29 31 34 36 38 43)	USP AT; US-P GPU B
45	L46	462	174/259.ccls.	USP AT; US-P GPU B
46	L47	433	46 not (11 20 23 27 29 31 34 36 38 43 45)	USP AT; US-P GPU B
47	L48	71	47 and (connector\$1 socket\$1)	USP AT; US-P GPU B
48	L49	59	(47 and pin\$1) not 48	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
49	L50	517	174/258.ccls.	USP AT; US-P GPU B
50	L51	470	50 not (11 20 23 27 29 31 34 36 38 43 45 48 49)	USP AT; US-P GPU B
51	L52	58	51 and (connector\$1 socket\$1)	USP AT; US-P GPU B
52	L53	95	51 and pin\$1	USP AT; US-P GPU B
53	L54	123	52 53	USP AT; US-P GPU B
54	L55	303	47 not (48 49)	USP AT; US-P GPU B
55	L56	347	51 not 54	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
56	L57	140	(55 56) and (flexible compliant)	USP AT; US-P GPU B
57	L58	523	439/65.ccls.	USP AT; US-P GPU B
58	L59	376	58 not (11 20 23 27 29 31 34 36 38 43 45 48 49 54 57)	USP AT; US-P GPU B

	L #	Hits	Search Text
1	L1	771	439/66.ccls.
2	L2	662	1 and (connector\$1 socket\$1 pin\$1)
3	L3	109	1 not 2